

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Applicant: Powell et al.	
App. No.: 08/887,680	Con. No.: 1324
Filed: July 3, 1997	Art Unit: 2155
Title: EXTENDING COMPUTER ARCHITECTURE FROM 32-BITS TO 64-BITS BY USING THE MOST SIGNIFICANT BIT OF THE STACK POINTER REGISTER TO INDICATE WORD SIZE	Examiner: Eng, David Y.

AMENDMENT AND RESPONSE TO FINAL OFFICE ACTION

MAIL STOP AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the final Office action dated February 12, 2007, please consider the following remarks and amend the above-identified application as follows:

Remarks begin on page 2 of this paper.